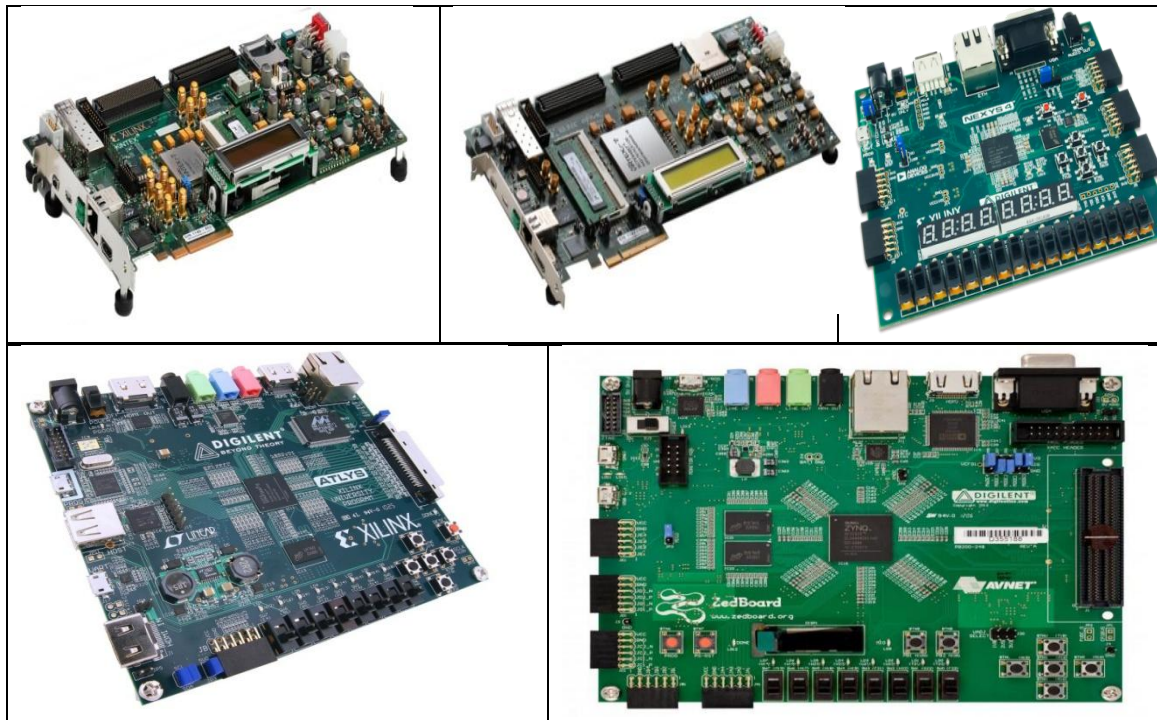


## System on Chip Laboratory- (SoC)

The goal of this laboratory is to design, rapid prototype and hardware-assisted verification of Complex DSP Algorithms related to Defense application. The application space is in Radar systems, EW, Digital Systems and other system on chip (SoC) designs. The laboratory supports the platform for conducting courses for the M.Tech Students. To be center of excellence and provide leadership in research related to design of Embedded Systems and system on chip (SoC) designs. The labs consist of Xilinx FPGA kits, DSP Kits, CUDA platform, Advanced micro controllers and Advanced System on chip kits.



### NAME OF COMPONENTS

- |  |  |
|--|--|
| <ol style="list-style-type: none"><li>1. Kintex 7, KC705</li><li>2. Virtex 7, VC707</li><li>3. Spartan 6 FPGA Development Board - Nexys 4</li><li>4. Spartan 6 FPGA Development Board - Atlys</li><li>5. ZED Board</li></ol> | <ol style="list-style-type: none"><li>6. PSoC 5 First Touch™ Starter Kit</li><li>7. PSoC MiniProg3 Program And Debug Kit</li><li>8. CUDA Platform</li><li>9. PSoC 3 Development Kit</li><li>1. Altera FPGA cyclone EP2C5T144 s</li></ol> |
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